



GCS

## **PROCESS SPECIFICATION: L3 MAC ADDRESS REQUIREMENTS**

### **1.0 SCOPE:**

This specification details the process for acquiring and assigning MAC Address numbers that affects board assemblies with a System-on-chip (SoC) host processor architecture.

### **2.0 ORDER OF PRECEDENCE:**

In the event of a conflict between the text of this document and the references cited herein, the text of this document takes precedence. Nothing in this document, however, supersedes applicable laws and regulations unless a specific exemption has been obtained.

### **3.0 BOARD IDENTIFICATION:**

- A. GCS assemblies that require MAC addresses will have a suffix of “M” in the part number. Example 0195-4530-01M
- B. MAC Addresses will be incorporated into the board serial number using the last six (6) digits from the address. For example, the provided MAC address is “00:10:27:OD:09:A9” the board serial number would be “GCSOD09A9

### **4.0 MAC ADDRESS NUMBER CONTROLS:**

- A. MAC addresses will be provided by L3-GCS Procurement (Buyer).
- B. Labels shall be formatted based on L3-GCS specification 1000-0901-01.
- C. Refer to the board test procedure for directions to program the MAC Address.